

Fig. 3

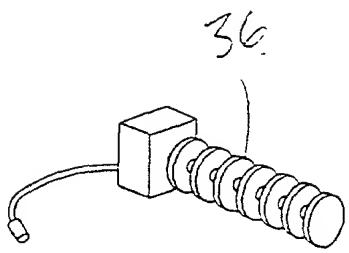


FIG.4A

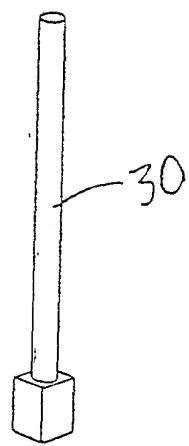


FIG.4B

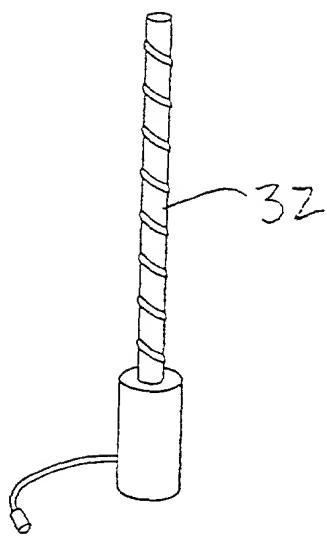


FIG.4C

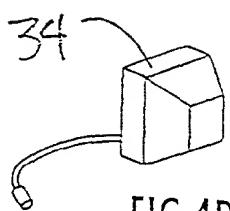
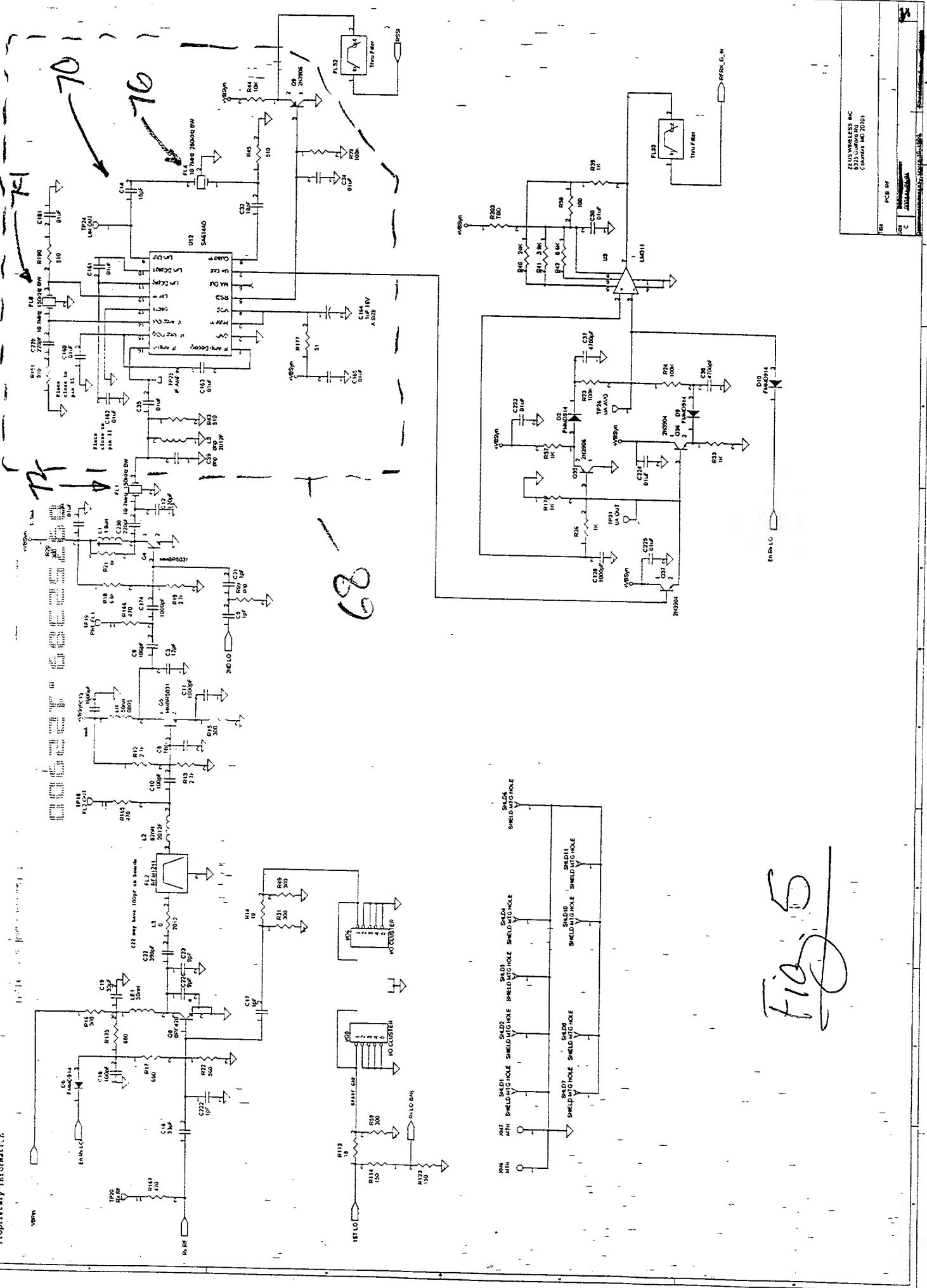


FIG.4D



BLOCK DIAGRAM

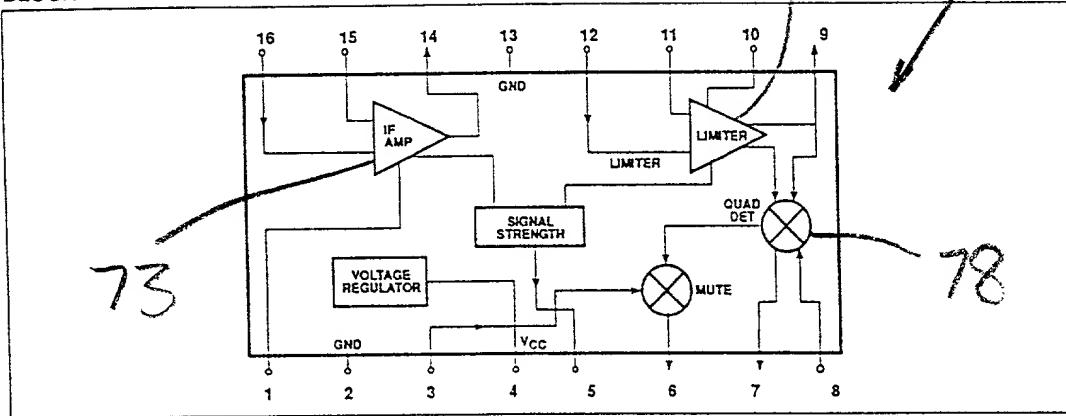


Fig. 6a

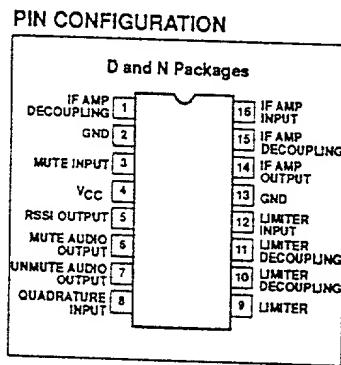


Fig. 6b

Fig. 7. A 65 MHz FSK receiver

